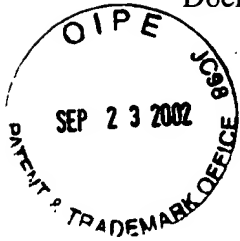


#5 / Election
9-25-02
PATENT Mullis

Docket No.: 4459-050



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Jen-Kuang FANG ET AL

Serial No. 09/883,216

Filed: June 19, 2001

For: SEMICONDUCTOR DEVICE HAVING BUMP ELECTRODES

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: Group Art Unit: 2811
:
: Examiner: S. Gebremariam
:

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

By Official Action mailed August 23, 2002, restriction to one of the following inventions is required under 35 USC 121:

- I. Claims 1-4, drawn to a method of forming integrated circuit device.
- II. Claims 5-6, drawn to a semiconductor integrated circuit device.

In response, Applicants hereby elect Claims 1-4 for examination in this case.

The foregoing election is made with traverse for the reason that the Examiner has not demonstrated how the process as claimed can be used to make other and materially different products from that recited in claims 5 and 6, the last sentence in paragraph 2 on page 2 of the Restriction Requirement notwithstanding. Accordingly, all claims are deemed to be properly

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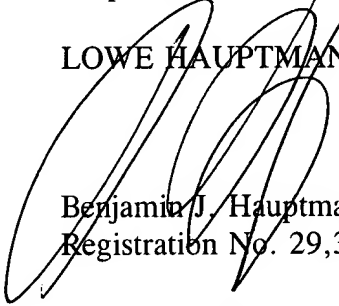
Serial No. 09/883,216

examined in this single application. In any event, early examination on the merits is courteously solicited.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 07-1337 and please credit any excess fees to such deposit account.

Respectfully submitted,

LOWE HAUPTMAN GILMAN & BERNER, LLP



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